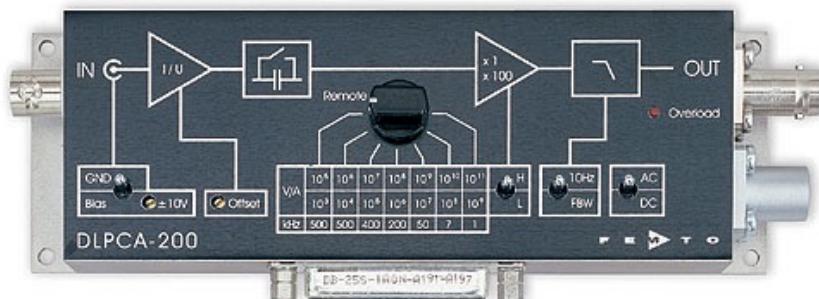


## Variable-Gain Low-Noise Current Amplifier

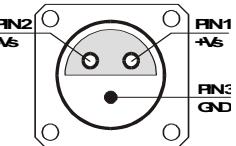


Features	<ul style="list-style-type: none"> <li>Transimpedance (Gain) switchable from <math>1 \times 10^3</math> to <math>1 \times 10^{11}</math> V/A</li> <li>Bandwidth DC / 1 Hz ... 500 kHz</li> <li>Bandwidth switchable to DC ... 10 Hz, for Low-Noise DC Measurements</li> <li>Bandwidth independent of Detector-Capacitance (up to 1 nF)</li> <li>BIAS-Voltage adjustable</li> <li>Protection against <math>\pm 3</math> kV Transients</li> <li>Local and Remote Control</li> </ul>									
Applications	<ul style="list-style-type: none"> <li>Photodiode- and Photomultiplier-Amplifier</li> <li>Scanning Tunneling Microscopy (STM)</li> <li>Spectroscopy</li> <li>Ionisation Detectors</li> <li>Preamplifier for Lock-Ins, A/D-Converters, etc.</li> </ul>									
Block Diagram	<p>BS01-0141-10</p>									
Specifications	<p><i>Test Conditions</i></p> <p><math>V_s = \pm 15 V, T_a = 25^\circ C</math></p> <table> <tr> <td data-bbox="276 1971 323 2001">Gain</td> <td data-bbox="561 1971 726 2001">Transimpedance</td> <td data-bbox="869 1971 1091 2001"><math>1 \times 10^3 \dots 1 \times 10^{11}</math> V/A</td> </tr> <tr> <td></td> <td data-bbox="561 2007 704 2037">Gain Accuracy</td> <td data-bbox="869 2007 917 2037"><math>\pm 1\%</math></td> </tr> <tr> <td></td> <td data-bbox="561 2041 657 2070">Gain Drift</td> <td data-bbox="869 2041 1037 2070">See Table below</td> </tr> </table>	Gain	Transimpedance	$1 \times 10^3 \dots 1 \times 10^{11}$ V/A		Gain Accuracy	$\pm 1\%$		Gain Drift	See Table below
Gain	Transimpedance	$1 \times 10^3 \dots 1 \times 10^{11}$ V/A								
	Gain Accuracy	$\pm 1\%$								
	Gain Drift	See Table below								

## Variable-Gain Low-Noise Current Amplifier

Frequency Response	Lower Cut-Off Frequency	DC / 1 Hz						
	Upper Cut-Off Frequency	Up to 500 kHz (See Table below), switchable to 10 Hz						
	Gain Flatness	±0.1 dB						
Input	Equ. Input Noise Current	See Table below (Value per √Hz, @ 100 Hz)						
	Equ. Input Noise Voltage	4 nV/√Hz (@ 100 Hz)						
	Input Offset Current Drift	See Table below						
	Input Bias Current	1 pA typ. (max. 3 pA)						
	Max. Input Current	See Table below (Value for Linear Amplification)						
	Input Offset Compensation	Adjustable by Offset-Trimmer and external control						
Performance depending on Gain Setting	Voltage, Max. Value see Table below							
	Gain Setting (Low Noise) (V/A)	10 <sup>3</sup>	10 <sup>4</sup>	10 <sup>5</sup>	10 <sup>6</sup>	10 <sup>7</sup>	10 <sup>8</sup>	10 <sup>9</sup>
	Upper Cut-Off Frequency (- 3 dB)	500 kHz	500 kHz	400 kHz	200 kHz	45 kHz	7 kHz	1.2 kHz
	Rise / Fall Time (10% - 90%)	700 ns	700 ns	900 ns	1.8 µs	8 µs	50 µs	300 µs
	Equ. Input Noise Current (f/√Hz)	20 pA	2.3 pA	460 fA	130 fA	43 fA	13 fA	4.3 fA
	Offset Current Drift (°C)	30 nA	3 nA	0.3 nA	27 pA	2.5 pA	0.2 pA	60 fA
	Gain Drift (°C)	0.008%	0.008%	0.008%	0.01%	0.01%	0.01%	0.02%
	Max. Input Current (±)	10 mA	1 mA	0.1 mA	10 µA	1 µA	0.1 µA	10 nA
	Max. Input Offset Compensat. (±)	100 µA	10 µA	1 µA	0.1 µA	10 nA	1 nA	0.1 nA
	DC Input Impedance (// 5 pF)	50 Ω	50 Ω	50 Ω	60 Ω	150 Ω	1 kΩ	10 kΩ
Output	Gain setting (High Speed) (V/A)	10 <sup>5</sup>	10 <sup>6</sup>	10 <sup>7</sup>	10 <sup>8</sup>	10 <sup>9</sup>	10 <sup>10</sup>	10 <sup>11</sup>
	Upper Cut-Off Frequency (- 3 dB)	500 kHz	500 kHz	400 kHz	200 kHz	45 kHz	7 kHz	1.2 kHz
	Rise / Fall Time (10% - 90%)	700 ns	700 ns	900 ns	1.8 µs	8 µs	50 µs	300 µs
	Equ. Input Noise Current (f/√Hz)	13 pA	1.8 pA	450 fA	130 fA	43 fA	13 fA	4.3 fA
	Offset Current Drift (°C)	30 nA	3 nA	0.3 nA	27 pA	2.5 pA	0.2 pA	60 fA
	Gain Drift (°C)	0.008%	0.008%	0.008%	0.01%	0.01%	0.01%	0.02%
Detector Bias	Max. Input Current (±)	100 µA	10 µA	1 µA	0.1 µA	10 nA	1 nA	0.1 nA
	Max. Input Offset Compensation	100 µA	10 µA	1 µA	0.1 µA	10 nA	1 nA	0.1 nA
	DC Input Impedance (// 5 pF)	50 Ω	50 Ω	50 Ω	60 Ω	150 Ω	1 kΩ	10 kΩ
Indicator LED	Output Voltage	±10 V (@10 kΩ Load)						
	Output Impedance	50 Ω						
	Max. Output Current	±30 mA						
Digital Control	Bias Voltage Range	±10 V, max. 22 mA, switchable to GND						
	Function	Overload						
	Control Input Voltage Range	Low: -0.8...+1.2V, High: 2.3...+12V						
Ext. Offset Control	Control Input Current	0 mA @ 0V, 1.5 mA @ +5 V, 4.5 mA @ +12V						
	Overload Output	Non Active: 0 V , max.-1mA, Active: 5.1 V, max. 7mA						
	Control Voltage Range	±10 V						
Power Supply	Offset Control Input Impedance	20 kΩ						
	Supply Voltage	± 15 V						
	Supply Current	+ 150 / -100 mA						
Case	Stabilized Power Supply Output	±12 V, max. 150 mA, +5V, max. 50 mA						
	Weight	320 gr. (0.74 lbs)						
Temperature Range	Material	AlMg4.5Mn, nickel-plated						
	Storage Temperature	-40 ... +100 °C						
	Operating Temperature	0 ... +60 °C						

# **Variable-Gain Low-Noise Current Amplifier**

Absolute Maximum Ratings	Signal Input Voltage Transient Input Voltage Control Input Voltage Power Supply Voltage	-16 V / + 12 V ±3 kV (from 200 pF Source) -5 V / +16 V ±22 V
Connectors	Input  Output  Power Supply	BNC, Isolated  BNC  LEMO Series 1S, 3-pin fixed Socket Pin 1: + 15V Pin 2: - 15V Pin 3: GND
Control Port		 <p>Sub-D 25-pin, female, Qual. Class 2</p> <p>Pin 1: +12V (Stabilized Power Supply Output)      Pin 2: -12V (Stabilized Power Supply Output)      Pin 3: AGND (Analog Ground)      Pin 4: +5V (Stabilized Power Supply Output)      Pin 5: Digital Output: Overload      Pin 6: Signal Output (connected to BNC)      Pin 7: NC      Pin 8: Input Offset Control Voltage      Pin 9: DGND (Ground for Digital Control Pin 10-14)      Pin 10: Digital Control Input: Gain, LSB      Pin 11: Digital Control Input: Gain      Pin 12: Digital Control Input: Gain, MSB      Pin 13: Digital Control Input: AC/DC      Pin 14: Digital Control Input: High Speed/Low Noise      Pin 15 – 25: NC</p>

## Variable-Gain Low-Noise Current Amplifier

Remote Control Operation	General	Remote Control Input Bits are opto-isolated and connected by logical OR to local switch setting. For remote control, set the corresponding local switch to "Remote", "AC" or "H" (High Speed) and select the wanted setting via a bit-code at the corresponding digital inputs. Mixed operation, e.g. local gain setting and remote controlled AC/DC setting, is also possible. Switch settings "FBW/10 Hz" and "Bias/GND" are not remote controllable.																																																
	Gain Setting	<table> <thead> <tr> <th>Low Noise Gain (V/A)</th> <th>High Speed Gain (V/A)</th> <th>Pin 10</th> <th>Pin 11</th> <th>Pin 12</th> </tr> <tr> <th>Pin 14=High</th> <th>Pin 14=Low</th> <th>LSB</th> <th></th> <th>MSB</th> </tr> </thead> <tbody> <tr> <td><math>10^3</math></td> <td><math>10^5</math></td> <td>Low</td> <td>Low</td> <td>Low</td> </tr> <tr> <td><math>10^4</math></td> <td><math>10^6</math></td> <td>High</td> <td>Low</td> <td>Low</td> </tr> <tr> <td><math>10^5</math></td> <td><math>10^7</math></td> <td>Low</td> <td>High</td> <td>Low</td> </tr> <tr> <td><math>10^6</math></td> <td><math>10^8</math></td> <td>High</td> <td>High</td> <td>Low</td> </tr> <tr> <td><math>10^7</math></td> <td><math>10^9</math></td> <td>Low</td> <td>Low</td> <td>High</td> </tr> <tr> <td><math>10^8</math></td> <td><math>10^{10}</math></td> <td>High</td> <td>Low</td> <td>High</td> </tr> <tr> <td><math>10^9</math></td> <td><math>10^{11}</math></td> <td>Low</td> <td>High</td> <td>High</td> </tr> </tbody> </table>					Low Noise Gain (V/A)	High Speed Gain (V/A)	Pin 10	Pin 11	Pin 12	Pin 14=High	Pin 14=Low	LSB		MSB	$10^3$	$10^5$	Low	Low	Low	$10^4$	$10^6$	High	Low	Low	$10^5$	$10^7$	Low	High	Low	$10^6$	$10^8$	High	High	Low	$10^7$	$10^9$	Low	Low	High	$10^8$	$10^{10}$	High	Low	High	$10^9$	$10^{11}$	Low	High
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Application Diagram	Photo Detector Biasing					<p>The diagram illustrates a photo detector biasing circuit. A photodiode is connected to a CURRENT INPUT terminal. This input is connected to the non-inverting input of an operational amplifier (labeled I/U). The inverting input of the I/U is connected to a Stabilized Bias Voltage source. The output of the I/U is connected to the non-inverting input of a second operational amplifier, which is labeled Active Current Limiting. The output of this second op-amp is connected to a Bias Buffer. The Bias Buffer has two outputs: +10 V and -10 V. A feedback loop from the output of the Bias Buffer is connected back to the inverting input of the Active Current Limiting op-amp. The AZ01-0140-1 reference code is located at the bottom right of the diagram area.</p>																																												

## Variable-Gain Low-Noise Current Amplifier

Dimensions

